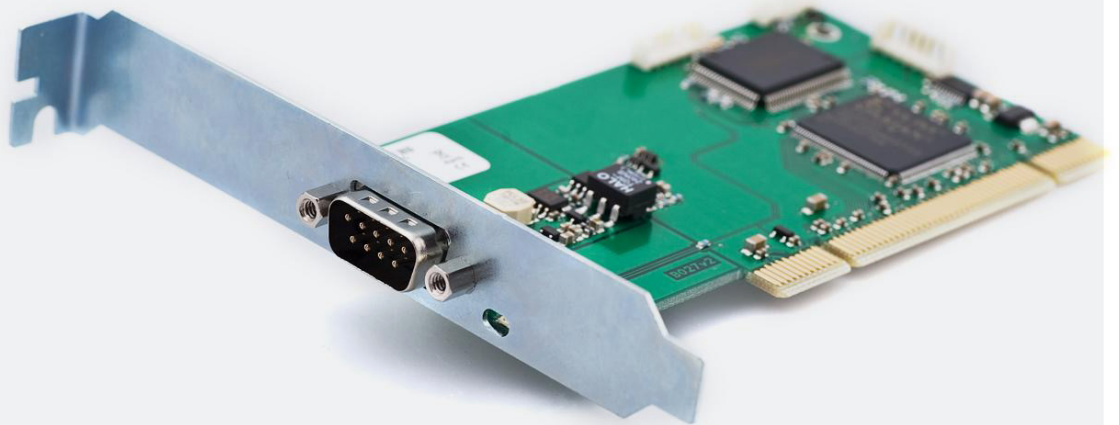
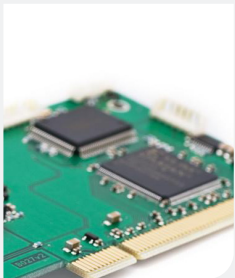




Learn more about  
this product



## Your Gateway to Efficient Connectivity

The Kvaser PCICanx II HS is a single channel, high speed CAN (controller area network) interface board for the PCI-X and PCI bus. It features an on-board microcontroller for offloading your main CPU and galvanic isolation for protection against voltage spikes.



### Warranty

2-Year warranty. See our general conditions and policies for details.



### Support

Free support for all products by contacting [support@kvaser.com](mailto:support@kvaser.com)



### EAN

73-30130-00344-6

## Major Features

- Quick and easy plug-and-play installation - no switches.
- Compliant with PCI 2.3.
- The board fits in 3.3V PCI-X, and 3.3V and 5V PCI busses.
- Fully software compatible with the discontinued PCICan II boards.
- Communicates with the PC through a fast DPRAM.
- CAN Controller is a Renesas M16C.
- Supports CAN 2.0 A and 2.0 B (active).
- High-speed ISO 11898 compliant driver circuit, supports bit rates up to 1 Mbit/s.
- Industry-standard 9-pin D-SUB connectors.
- Pin assignment according to CiA-DS102.
- 16 MHz CAN oscillator frequency.
- Galvanic isolation between the CAN-controller and the CAN-driver.
- The Kvaser PCICanx II family boards can optionally be delivered with exchangeable CAN drivers, supporting e.g. single-wire CAN.
- Includes free of charge low profile bracket.
- Compatible with J1939, CANopen, NMEA 2000® and DeviceNet. Higher layer protocol translation handled by the user's application. For software support please see our Technical Associates products and our Software Download page ([www.kvaser.com](http://www.kvaser.com)).

## Support

Documentation, Kvaser CANlib SDK and drivers can be downloaded for free at [www.kvaser.com/downloads](http://www.kvaser.com/downloads).

Kvaser CANlib SDK is a free resource that includes everything you need to develop software for the Kvaser CAN interfaces. Includes full documentation and many program samples, written in C, C++, C#, Delphi, Visual Basic, Python and t programming language.

Kvaser CAN hardware is built around the same common software API. Applications developed using one device type will run without modification on other device types.

## Technical Data

<b>CAN FD</b>	No
<b>Certificates</b>	CE, RoHS
<b>Channels</b>	1
<b>Connectors</b>	D-SUB 9
<b>Current Consumption</b>	Approximately 1 W (200 mA)
<b>Dimensions</b>	Low profile, 121 x 64 mm
<b>Error Counters Reading</b>	Yes
<b>Error Frame Detection</b>	Yes
<b>Error Frame Generation</b>	Yes
<b>Galvanic Isolation</b>	Yes
<b>Interfaces</b>	CAN, PCI
<b>IP Class</b>	IP00
<b>Maximum Bitrate</b>	1000 kbps
<b>Minimum Bitrate</b>	20 kbps
<b>Msgrate Rx Max</b>	14000
<b>Msgrate Tx Max</b>	8000
<b>On-Board Buffer</b>	Yes
<b>On-Board Rx Buffer</b>	Yes
<b>On-Board Tx Buffer</b>	Yes
<b>Operating Systems</b>	Linux, Windows <sup>1</sup>
<b>Silent Mode</b>	Yes
<b>Temperature Range</b>	-40 to +85 °C
<b>Weight</b>	57 g

<sup>1</sup> Windows 7, 8, 10 (IA-32 and x86-64)  
Windows 11 (x86-64)